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**1 Folded-cascode transimpedance amplifier for burst-mode applications**

Schneider, K.; Zimmermann, H.;  
Electron Devices for Microwave and Optoelectronic Applications, 2002. EDMO  
The 10th IEEE International Symposium on , 18-19 Nov. 2002  
Pages:294 - 299

[\[Abstract\]](#)   [\[PDF Full-Text \(388 KB\)\]](#)   IEEE CNF

**2 Differential transimpedance amplifiers for communications systems based on common-gate topology**

Martinez-Castillo, J.; Diaz-Sanchez, A.; Linares-Aranda, M.;  
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium  
on , Volume: 2 , 26-29 May 2002  
Pages:II-97 - II-100 vol.2

[\[Abstract\]](#)   [\[PDF Full-Text \(371 KB\)\]](#)   IEEE CNF

**3 40-Gb/s optical receiver IC chipset - including a transimpedance amplifier, a differential amplifier, and a decision circuit - using GaAs-t HBT technology**

Amamiya, Y.; Suzuki, Y.; Kawanaka, M.; Hosoya, K.; Yamazaki, Z.; Mamada, T.; Takahashi, H.; Wada, S.; Kato, T.; Ikenaga, Y.; Tanaka, S.; Takeuchi, T.; Hidemoto, T.;  
Microwave Symposium Digest, 2002 IEEE MTT-S International , Volume: 1 , 2  
June 2002  
Pages:87 - 90

[\[Abstract\]](#)   [\[PDF Full-Text \(463 KB\)\]](#)   IEEE CNF

**4 A 2.5-mW SOS CMOS optical receiver for chip-to-chip interconnect**

Apsel, A.; Zhongtao Fu; Andreou, A.G.;

Lightwave Technology, Journal of , Volume: 22 , Issue: 9 , Sept. 2004  
Pages:2149 - 2157

[Abstract] [PDF Full-Text (1192 KB)] IEEE JNL

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**5 1-Gb/s 80-dB/spl Omega/ fully differential CMOS transimpedance amplifier in multichip on oxide technology for optical interconnects**  
*Sung Min Park; Jaeseo Lee; Hoi-Jun Yoo;*  
Solid-State Circuits, IEEE Journal of , Volume: 39 , Issue: 6 , June 2004  
Pages:971 - 974

[Abstract] [PDF Full-Text (344 KB)] IEEE JNL

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**6 40-GHz transimpedance amplifier with differential outputs using Inl InGaAs heterojunction bipolar transistors**  
*Wu, C.Q.; Soviero, E.A.; Massey, B.;*  
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**7 SiGe differential transimpedance amplifier with 50-GHz bandwidth**  
*Weiner, J.S.; Leven, A.; Houtsma, V.; Baeyens, Y.; Young-Kai Chen; Paschke, Yang Yang; Frackoviak, J.; Wei-Jer Sung; Tate, A.; Reyes, R.; Kopf, R.F.; Weimann, N.G.;*  
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**8 Characterization of a 16-channel optical/electronic selector for fast packet-switched WDMA networks**  
*Tong, F.; Li, C.-S.; Stevens, A.E.; Kwark, Y.H.;*  
Photonics Technology Letters, IEEE , Volume: 6 , Issue: 8 , Aug. 1994  
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[Abstract] [PDF Full-Text (276 KB)] IEEE JNL

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**9 Wideband CMOS transimpedance amplifier**  
*Kossel, M.; Menolfi, C.; Morf, T.; Schmatz, M.; Toifl, T.;*  
Electronics Letters , Volume: 39 , Issue: 7 , 3 April 2003  
Pages:587 - 588

[Abstract] [PDF Full-Text (261 KB)] IEE JNL

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**10 40 Gbit/s transimpedance amplifier in SiGe bipolar technology for 1 receiver in optical-fibre TDM links**  
*Mullrich, J.; Meister, T.F.; Rest, M.; Bogner, W.; Schopflin, A.; Rein, H.-M.;*  
Electronics Letters , Volume: 34 , Issue: 5 , 5 March 1998  
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**11 A 20mW 85dB/spl Omega/ 1.25Gb/s CMOS transimpedance amplifier with photodiode capacitance cancellation***Chia-Ming Tsai;*

VLSI Circuits, 2004. Digest of Technical Papers. 2004 Symposium on , 17-19 : 2004

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[\[Abstract\]](#) [\[PDF Full-Text \(287 KB\)\]](#) [IEEE CNF](#)**12 A 10Gb/s SiGe transimpedance amplifier using a pseudo-differential input stage and a modified Cherry-Hooper amplifier***Maxim, A.;*

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[\[Abstract\]](#) [\[PDF Full-Text \(301 KB\)\]](#) [IEEE CNF](#)**14 A 10 Gbase-LX4 receiver front end transimpedance amplifier and limiting amplifier***Hung-Chieh Tsai; Jyh-Yih Yeh; Wei-Hsuan Tu; Tai-Cheng Lee; Chorng-Kuang Wang;*

Circuits and Systems, 2004. ISCAS '04. Proceedings of the 2004 International Symposium on , Volume: 4 , 23-26 May 2004

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[\[Abstract\]](#) [\[PDF Full-Text \(695 KB\)\]](#) [IEEE CNF](#)**15 High-speed optoelectronics receivers in SiGe***Gupta, A.; Levitan, S.P.; Selavo, L.; Chiarulli, D.M.;*

VLSI Design, 2004. Proceedings. 17th International Conference on , 2004

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